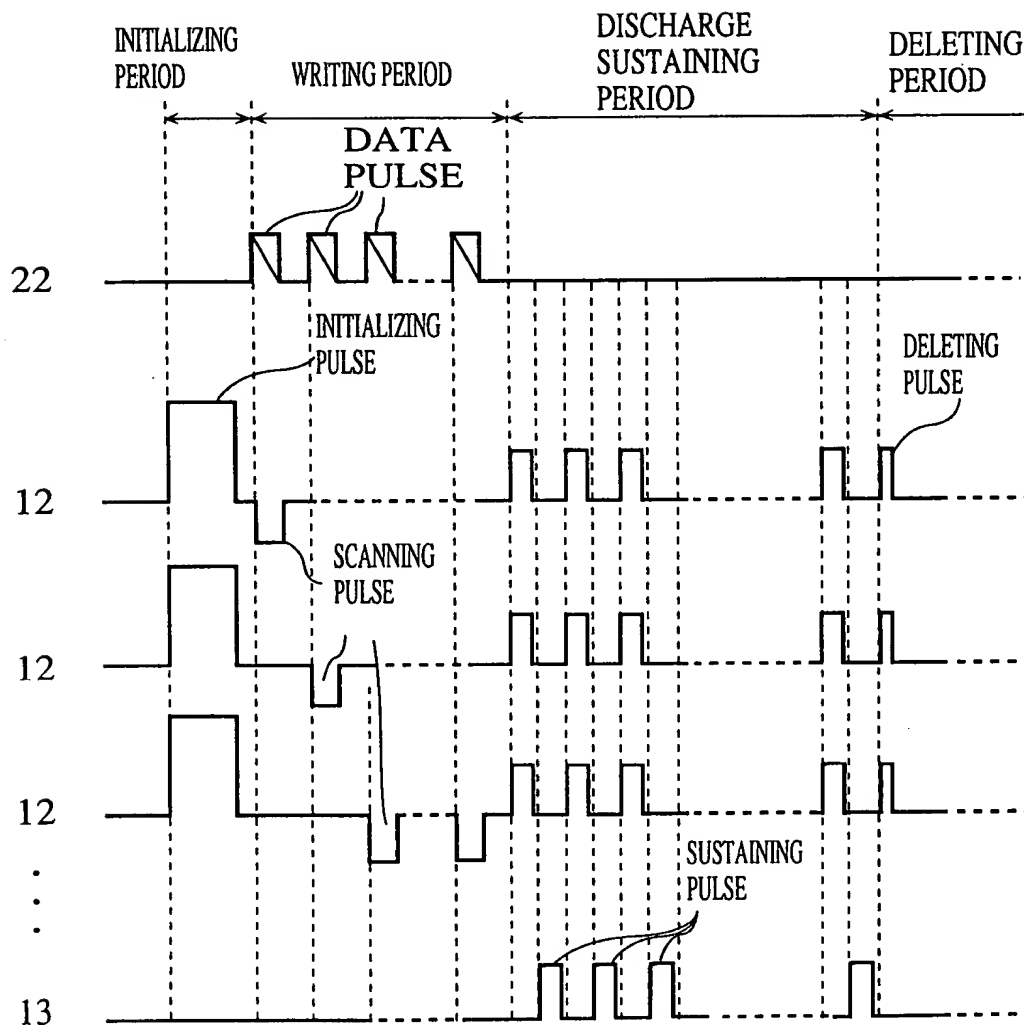


A block diagram of a plasma display panel (100). The panel (1) is a rectangular grid of cells. It is connected to a SCAN DRIVER 102 on the left, a SUSTAIN DRIVER 103 on the right, and a DATA DRIVER 104 at the bottom. The panel is also connected to a PANEL CONTROL CIRCUIT (101) at the bottom. The panel is divided into three vertical sections: a left section (12), a central section (13), and a right section (13). The panel is also connected to a DATA DRIVER 104 at the bottom. The panel is also connected to a PANEL CONTROL CIRCUIT (101) at the bottom. The panel is also connected to a DATA DRIVER 104 at the bottom. The panel is also connected to a PANEL CONTROL CIRCUIT (101) at the bottom.

FIG.3



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FIG.4

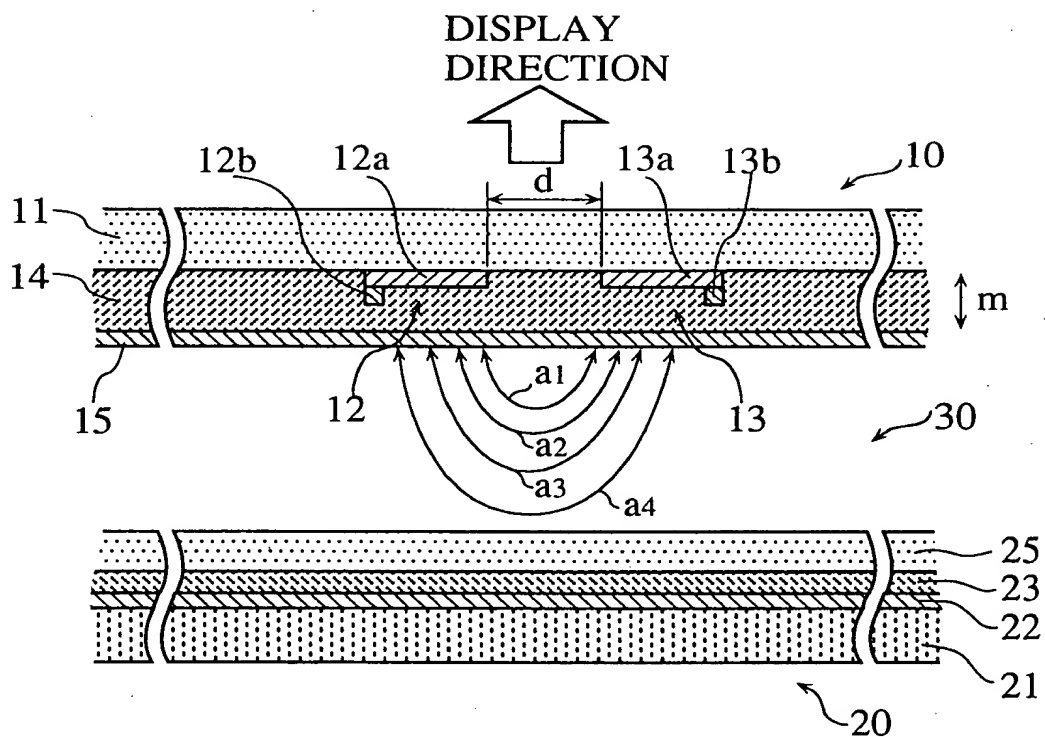
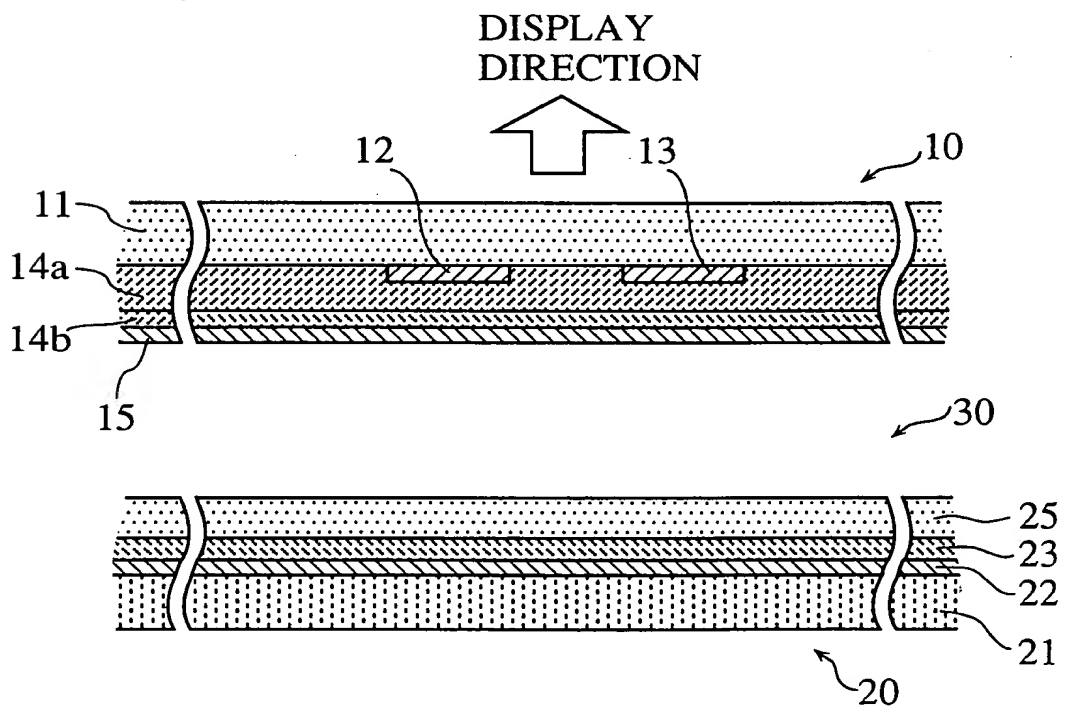


FIG.5



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FIG.7

